

	<b>L #</b>	<b>Hit</b>	<b>S arch T xt</b>	<b>DB</b>
<b>1</b>	<b>L1</b>	<b>3060</b>	<b>(257/691,723,724,777,924).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>1885</b>	<b>(361/760,763,782,783).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>4740</b>	<b>1 2</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L4</b>	<b>292</b>	<b>3 and @pd&gt;=20030407</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>2</b>	<b>4 and (charg\$3 with pump\$3)</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L6</b>	<b>87</b>	<b>4 and (capacitor\$1 condenser\$1 inductor\$1 (passive adj (component\$1 device\$1 element\$1)))</b>	<b>USP AT; US-P GPU B</b>